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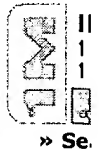




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<input type="checkbox"/>	L20	L19 and circular\$3	1
<input type="checkbox"/>	L19	L18 and l4 and l1	6
<input type="checkbox"/>	L18	scan\$3 near5 single	14186
<input type="checkbox"/>	L17	l9 and l10	2
<input type="checkbox"/>	L16	l8 and l9 and l10	0
<input type="checkbox"/>	L15	L14 and (l10 or l9 or l8)	0
<input type="checkbox"/>	L14	circular and l4 and l1 l	46
<input type="checkbox"/>	L13	L12 and l1	0
<input type="checkbox"/>	L12	L11 and l3	20
<input type="checkbox"/>	L11	access adj1 operation	5325
<input type="checkbox"/>	L10	root adj1 pointer	120
<input type="checkbox"/>	L9	(next adj1 pointer) near3 field	91
<input type="checkbox"/>	L8	first adj1 member	30464
<input type="checkbox"/>	L7	l4 and l5 and l1	2
<input type="checkbox"/>	L6	L5 same l4	2
<input type="checkbox"/>	L5	circular near2 singly	31
<input type="checkbox"/>	L4	linked adj1 list	5528
<input type="checkbox"/>	L3	circular near3 list	497
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Parallel and Distributed Systems, IEEE Transactions on , Volume: 8 , Issue: 6 , June 1997

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**1 On the doubly-linked list protocol for distributed shared memory multiprocessor systems**
*Lau, A.C.K.; Leung, K.H.W.; Yung, N.H.C.; Cheung, Y.S.;*

Algorithms and Architectures for Parallel Processing, 1995. ICAPP 95. IEEE First International Conference on , Volume: 1 , 19-21 April 1995

Pages:293 - 302 vol.1

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**2 Fault-tolerant architectures for shared buffer memory switch**
*Yeong-Fong Lin; Shung, C.B.;*

Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium , Volume: 3 , 30 May-2 June 1994

Pages:61 - 64 vol.3

[\[Abstract\]](#)   [\[PDF Full-Text \(292KB\)\]](#)   IEEE CNF

**3 Linked-list visualization for debugging**
*Shimomura, T.; Isoda, S.;*

Software, IEEE , Volume: 8 , Issue: 3 , May 1991

Pages:44 - 51

[\[Abstract\]](#)   [\[PDF Full-Text \(636KB\)\]](#)   IEEE JNL

**4 Distributed-directory scheme: Stanford distributed-directory protocol**
*Thapar, M.; Delagi, B.;*

Computer , Volume: 23 , Issue: 6 , June 1990

Pages:78 - 80

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**5 Web page reuse techniques: a dynamic referential navigational guide**
*Eng Huat Ng; Wade, S.; Ghaoui, C.;*

Euromicro Conference, 2000. Proceedings of the 26th , Volume: 2 , 5-7 Sept.

Pages:72 - 77 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(400KB\)\]](#) IEEE CNF

#### 6 **Rapid determination of co-occurrence texture features**

*Clausi, D.A.; Yongping Zhao;*

Geoscience and Remote Sensing Symposium, 2001. IGARSS '01. IEEE 2001 International , Volume: 4 , 9-13 July 2001  
Pages:1880 - 1882 vol.4

[\[Abstract\]](#) [\[PDF Full-Text \(84KB\)\]](#) IEEE CNF

#### 7 **Competitive access time via dynamic storage rearrangement**

*Fiat, A.; Mansour, Y.; Rosen, A.; Waarts, O.;*

Foundations of Computer Science, 1995. Proceedings., 36th Annual Symposium on , 23-25 Oct. 1995  
Pages:392 - 401

[\[Abstract\]](#) [\[PDF Full-Text \(872KB\)\]](#) IEEE CNF

#### 8 **Parallelizing while loops for multiprocessor systems**

*Rauchwerger, L.; Padua, D.;*

Parallel Processing Symposium, 1995. Proceedings., 9th International , 25-28 1995  
Pages:347 - 356

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#### 9 **Checking linked data structures**

*Amato, N.M.; Loui, M.C.;*

Fault-Tolerant Computing, 1994. FTCS-24. Digest of Papers., Twenty-Fourth International Symposium on , 15-17 June 1994  
Pages:164 - 173

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#### 10 **An orientation and resolution independent texture classifier in segmentation of images of unknown rotation and scale**

*You, J.; Cohen, H.A.;*

Pattern Recognition, 1992. Vol.III. Conference C: Image, Speech and Signal Analysis, Proceedings., 11th IAPR International Conference on , 30 Aug.-3 Sep 1992  
Pages:49 - 52

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#### 11 **A framework to capture dynamic data structures in pointer-based c**

*Corbera, F.; Asenjo, R.; Zapata, E.L.;*

Parallel and Distributed Systems, IEEE Transactions on , Volume: 15 , Issue: 2 , Feb 2004  
Pages:151 - 166

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**12 Design of multi-invariant data structures for robust shared access multiprocessor systems**

*I-Ling Yen; Bastani, F.B.; Taylor, D.J.;*

Software Engineering, IEEE Transactions on , Volume: 27 , Issue: 3 , March 2000  
Pages:193 - 207

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**13 An efficient tree cache coherence protocol for distributed shared memory multiprocessors**

*Yeimkuan Chang; Bhuyan, L.N.;*

Computers, IEEE Transactions on , Volume: 48 , Issue: 3 , March 1999  
Pages:352 - 360

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**14 A fast method to determine co-occurrence texture features**

*Clausi, D.A.; Jernigan, M.E.;*

Geoscience and Remote Sensing, IEEE Transactions on , Volume: 36 , Issue: 1 , Jan. 1998  
Pages:298 - 300

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**15 A circular list based mutual exclusion scheme for large shared-memory multiprocessors**

*Fu, S.S.; Nian-Feng Tzeng;*

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